

WHAT IS CLAIMED IS:

1. An integrated circuit for compressing peak sample values in spread spectrum signals, comprising:

a plurality of peak detection and cancellation circuits arranged in a sequence, a first peak detection and cancellation circuit having an input coupled to receive a spread spectrum symbol stream, at least a second peak detection and cancellation circuit having an input coupled to the output of a preceding peak detection and cancellation circuit in the sequence, each peak detection and cancellation circuit for applying a cancellation pulse to a received symbol stream responsive to detecting a peak amplitude in the received symbol stream exceeding a threshold, and for presenting a compressed symbol stream including the received symbol stream and cancellation pulse at its output.

2. The integrated circuit of claim 1, further comprising:

an output limiter, coupled to the output of the last of the peak detection and cancellation circuits in the sequence, for reducing residual peak amplitudes in the compressed symbol stream from the last of the peak detection and cancellation circuits in the sequence.

3. The integrated circuit of claim 1, further comprising:

a digital downampler, for receiving the spread spectrum symbol stream and forwarding a subset of the symbols in the spread spectrum symbol stream to the input of the first of the peak detection and cancellation circuits in the sequence.

4. The integrated circuit of claim 1, wherein at least one of the plurality of peak detection and cancellation circuits comprises:

a peak detector circuit, for identifying a peak location and a filter value corresponding to an amplitude at the peak location;

5 at least one cancellation pulse generator, for producing a cancellation pulse corresponding to the identified peak location and the corresponding filter value;

a delay stage for delaying the received symbol stream; and

an adder, for combining the delayed received symbol stream and the cancellation pulse.

5. The integrated circuit of claim 4, wherein the peak detector circuit comprises

an interpolating circuit, for generating a curve-fitting estimate of values near a sample point;

5 an evaluation circuit, for determining the peak location from the curve-fitting estimate;

a value computation circuit, for evaluating the amplitude at the peak location;

a qualifier, for comparing the evaluated amplitude against a threshold value; and

10 a filter generator, for producing the filter value from the evaluated amplitude.

6. The integrated circuit of claim 5, wherein the interpolating circuit comprises a Farrow filter bank.

7. The integrated circuit of claim 5, wherein the evaluation circuit is a binary search function.

8. The integrated circuit of claim 5, wherein the peak detector circuit further comprises:

a peak pre-qualifier, for comparing a magnitude for each sample point in the received symbol stream with magnitudes of one or more neighboring samples, and
5 for pre-qualifying a sample point if its magnitude is greater than that of the one or more neighboring samples;

wherein the interpolating circuit is a curve-fitting estimate of values near a sample point responsive to the peak pre-qualifier pre-qualifying the sample point.

9. The integrated circuit of claim 5, wherein the qualifier is also for issuing a qualifying signal for a first sample point responsive to the evaluated amplitude of the first sample point exceeding a threshold value in combination with no subsequent sample points in a selected interval having a larger evaluated amplitude than that of the
5 first sample point.

10. The integrated circuit of claim 4, wherein the at least one of the plurality of peak detection and cancellation circuits comprises a plurality of cancellation pulse generators.

11. The integrated circuit of claim 10, wherein each of the plurality of cancellation pulse generators comprises:

a look-up table memory for storing a plurality of FIR pulse coefficients;
computational circuitry, coupled to the look-up table memory and to the
5 peak detector circuit, for producing a data stream corresponding to the combination of the FIR pulse coefficients with an offset corresponding to the peak location; and
gain scaling circuitry, for scaling the data stream responsive to the filter value from the filter generator of the peak detector circuit.

12. The integrated circuit of claim 11, wherein the filter value comprises in-phase and quadrature-phase components;

and wherein the output of the gain scaling circuitry comprises in-phase and quadrature-phase components.

13. The integrated circuit of claim 10, wherein each of the plurality of cancellation pulse generators comprises:

infinite impulse response circuitry, for producing a data stream corresponding to an offset corresponding to the peak location.

14. The integrated circuit of claim 10, further comprising:

an unprocessed peak counter, for counting a number of peak amplitudes not processed by the plurality of cancellation pulse generators in each of the plurality of peak detection and cancellation circuits.

15. A method of transmitting a spread spectrum communications signal, comprising the steps of:

applying at least one peak compression pulse to the spread spectrum signal at a first peak sample point, the magnitude of the signal at the first peak sample point exceeding a peak qualifying threshold, to produce a peak-compressed symbol stream;

repeating, at least once, the applying step on the peak-compressed symbol stream;

amplifying an analog modulated signal corresponding to a peak-compressed symbol stream from the last of the repeated applying steps to produce the signal to be transmitted.

16. The method of claim 15, further comprising:
 - before the amplifying step, output limiting the peak-compressed symbol stream from the last of the repeated applying steps.
17. The method of claim 15, further comprising:
 - downsampling the spread spectrum signal before the step of applying at least one peak compression pulse to the spread spectrum signal at a first peak sample point.
18. The method of claim 15, wherein each applying step comprises:
 - identifying a peak location and a filter value corresponding to an amplitude at a peak location in the signal;
 - producing a cancellation pulse corresponding to the identified peak location and the corresponding filter value;
 - delaying the spread spectrum signal to match the peak location; and
 - combining the delayed received signal and the cancellation pulse.
19. The method of claim 18, wherein the identifying step comprises:
 - generating a curve-fitting estimate over a delay interval near a sample point;
 - determining the peak location within the delay interval from the curve-fitting estimate;
 - evaluating the amplitude at the determined peak location;
 - comparing the evaluated amplitude against a peak qualifying threshold; and
 - producing the filter value responsive to the evaluated amplitude.

20. The method of claim 19, wherein the step of generating a curve-fitting estimate comprises applying the value of the sample point and a plurality of neighboring sample points to a Farrow filter.

21. The method of claim 19, wherein the step of determining the peak location from the curve-fitting estimate comprises performing a binary search of amplitudes of the estimated curve over the delay interval.

22. The method of claim 19, further comprising:

comparing a magnitude of each sample point in the received symbol stream with magnitudes of one or more neighboring samples;

5 pre-qualifying a sample point if its magnitude is greater than that of the one or more neighboring samples;

wherein the identifying step is performed for pre-qualified sample points.

23. The method of claim 19, further comprising:

responsive to the comparing step determining that the evaluated amplitude of a first sample point exceeds the peak qualifying threshold, comparing the evaluated amplitudes of peak sample points over a selected number of subsequent 5 sample points;

wherein the producing step is performed for the first sample point responsive to no peak sample points in the selected number of subsequent sample points having a larger evaluated amplitude than that of the first sample point;

and further comprising:

10 responsive to the comparing step determining that the evaluated amplitude of a second sample point within the selected number of subsequent sample points has a larger evaluated amplitude than that of the first sample point, inhibiting producing of the filter value for the first sample point and then repeating the comparing

the evaluated amplitudes of peak sample points over a selected number of subsequent sample points relative to the second sample point.

24. The method of claim 18, wherein the step of producing a cancellation pulse is performed within each applying step simultaneously for a plurality of identified peak locations in the symbol stream.

25. The method of claim 18, wherein the step of producing a cancellation pulse comprises:

accessing a look-up table memory to retrieve FIR pulse coefficients;
combining the FIR pulse coefficients with an offset corresponding to the peak location to produce a data stream; and
gain scaling the data stream responsive to the filter value.

26. The method of claim 25, wherein the gain scaling step comprises applying in-phase and quadrature-phase components of the filter value to the data stream to produce in-phase and quadrature-phase components of the data stream.

27. The method of claim 18, wherein the step of generating a cancellation pulse comprises:

applying an infinite impulse response filter to an offset corresponding to the peak location.

28. A wireless base station for transmitting spread spectrum signals corresponding to a plurality of communications channels, comprising:

at least one coder/decoder for generating a spread spectrum signal over a plurality of channels, the signal being arranged in the form of a digital symbol stream;

5 a plurality of peak detection and cancellation circuits arranged in a sequence, a first peak detection and cancellation circuit having an input coupled to receive the symbol stream, at least a second peak detection and cancellation circuit

having an input coupled to the output of a preceding peak detection and cancellation circuit in the sequence, each peak detection and cancellation circuit for applying a

10 cancellation pulse to a received symbol stream responsive to detecting a peak amplitude in the received symbol stream exceeding a threshold, and for presenting a compressed symbol stream including the received symbol stream and cancellation pulse at its output;

a digital-to-analog converter for converting the compressed symbol

15 stream to an analog signal;

modulation circuitry for producing a modulated signal, corresponding to the analog signal, at a carrier frequency; and

a power amplifier, for amplifying the modulated signal for transmission.

29. The base station of claim 28, further comprising:

an output limiter, coupled to the output of the last of the peak detection and cancellation circuits in the sequence, for reducing residual peak amplitudes in the compressed symbol stream from the last of the peak detection and cancellation circuits

5 in the sequence.

30. The base station of claim 28, wherein at least one of the plurality of peak detection and cancellation circuits comprises:

a peak detector circuit, for identifying a peak location and a filter value corresponding to an amplitude at the peak location;

5 at least one cancellation pulse generator, for producing a cancellation pulse corresponding to the identified peak location and the corresponding filter value;

a delay stage for delaying the received symbol stream; and

an adder, for combining the delayed received symbol stream and the cancellation pulse.

31. The base station of claim 30, wherein the peak detector circuit comprises an interpolating circuit, for generating a curve-fitting estimate of values near a sample point;

an evaluation circuit, for determining the peak location from the curve-fitting estimate;

5 a value computation circuit, for evaluating the amplitude at the peak location;

a qualifier, for comparing the evaluated amplitude against a threshold value; and

10 a filter generator, for producing the filter value from the evaluated amplitude.

32. The base station of claim 31, wherein the interpolating circuit comprises a Farrow filter bank.

33. The base station of claim 31, wherein the evaluation circuit is a binary search function.

34. The base station of claim 30, wherein the peak detector circuit further comprises:

5 a peak pre-qualifier, for comparing a magnitude for each sample point in the received symbol stream with magnitudes of one or more neighboring samples, and for pre-qualifying a sample point if its magnitude is greater than that of the one or more neighboring samples;

wherein the interpolating circuit is a curve-fitting estimate of values near a sample point responsive to the peak pre-qualifier pre-qualifying the sample point.

35. The base station of claim 30, wherein the qualifier is also for issuing a qualifying signal for a first sample point responsive to the evaluated amplitude of the

first sample point exceeding a threshold value in combination with no subsequent sample points in a selected interval having a larger evaluated amplitude than that of the
5 first sample point.

36. The base station of claim 28, wherein the at least one of the plurality of peak detection and cancellation circuits comprises a plurality of cancellation pulse generators; and wherein each of the plurality of cancellation pulse generators comprises:
a look-up table memory for storing a plurality of FIR pulse coefficients;
5 computational circuitry, coupled to the look-up table memory and to the peak detector circuit, for producing a data stream corresponding to the combination of the FIR pulse coefficients with an offset corresponding to the peak location; and
gain scaling circuitry, for scaling the data stream responsive to the filter value from the filter generator of the peak detector circuit.

37. The base station of claim 36, wherein the filter value comprises in-phase and quadrature-phase components;
and wherein the output of the gain scaling circuitry comprises in-phase and quadrature-phase components.

38. The base station of claim 35, wherein each of the plurality of cancellation pulse generators comprises:
infinite impulse response circuitry, for producing a data stream corresponding to an offset corresponding to the peak location.

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